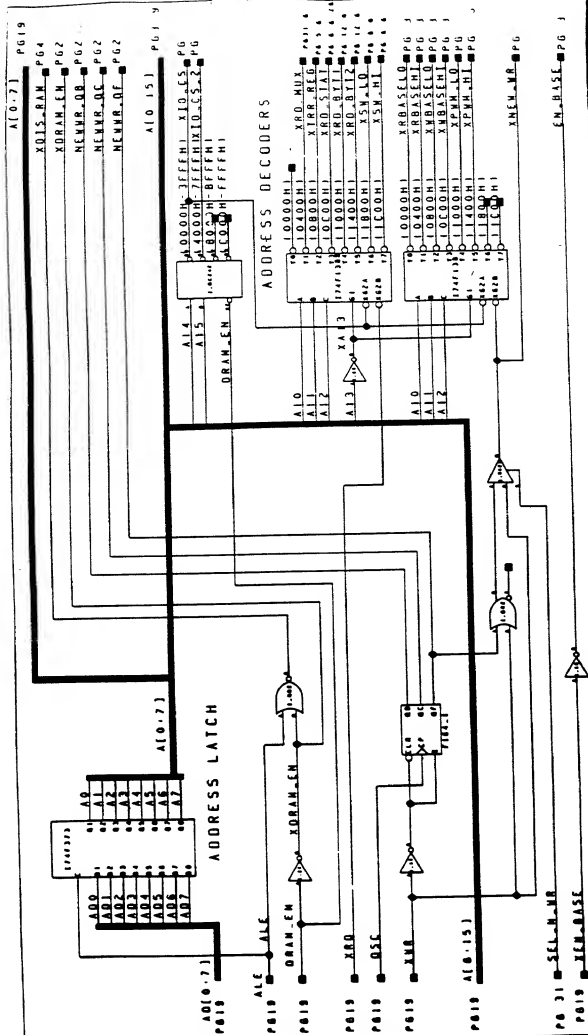


41383/RRT/S787

SYSTEM AND METHOD FOR TRANSMITTING AND UTILIZING
ELECTRONIC PROGRAMS GUIDE INFORMATION

APPENDIX A

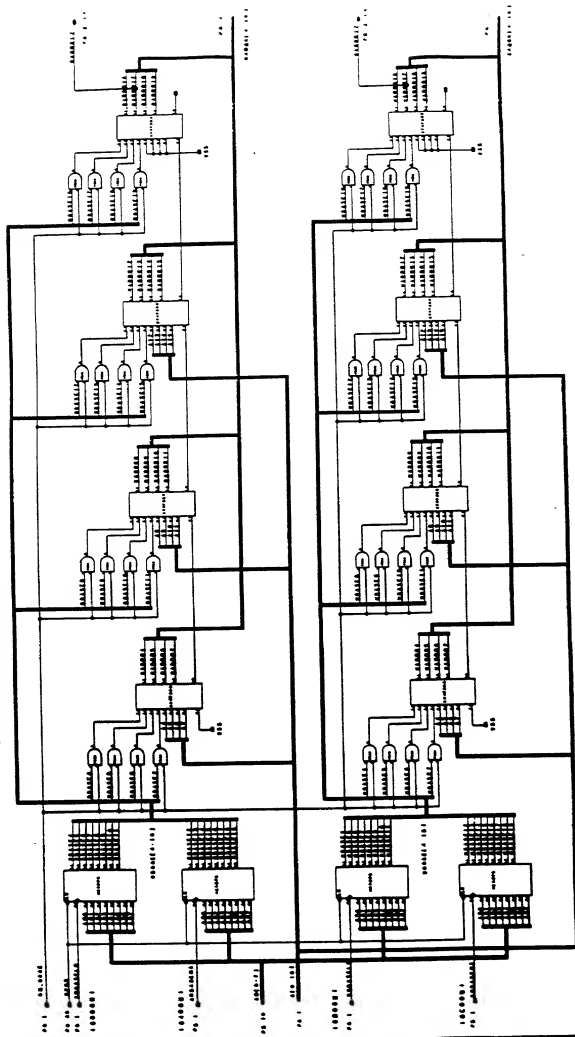
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CONTENTS: IMPBGL1-9



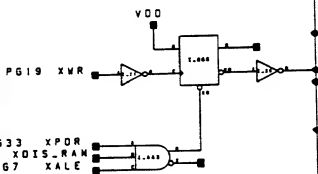
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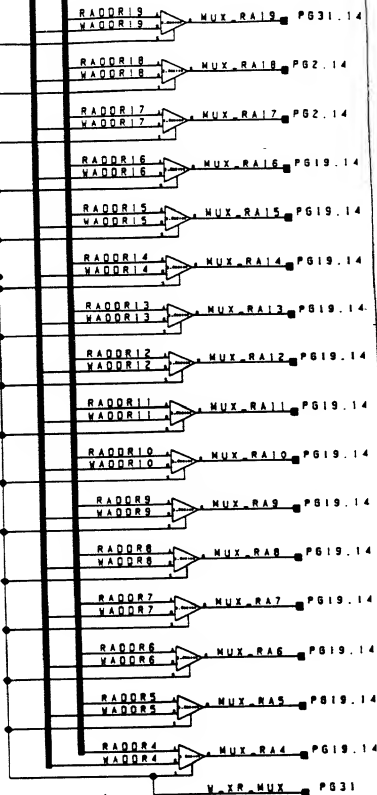
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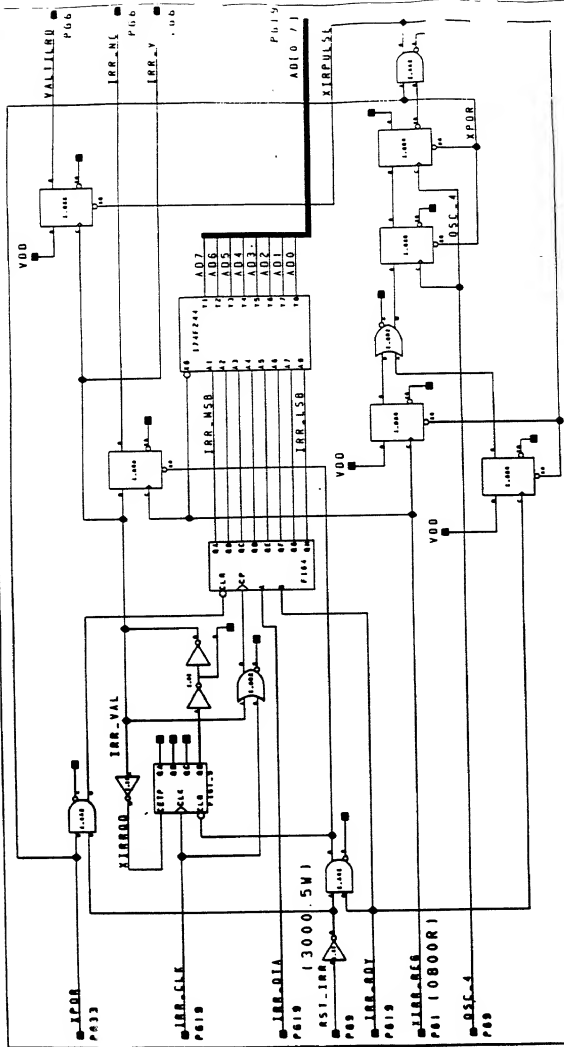
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SCHEMATIC DBE 1200
P/N 10-01-00003

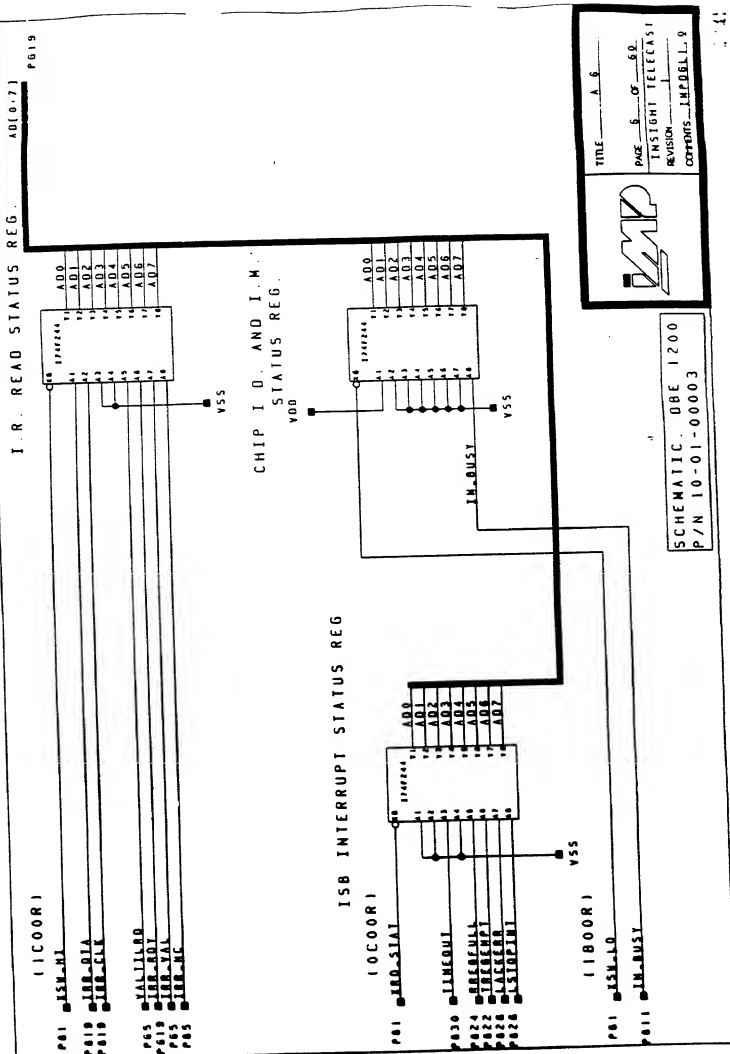
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PAGE 4 OF 60
INSIGHT TELECAST
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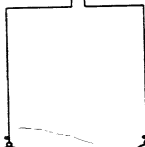
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PAGE 7 OF 60
INSIGHT TELECAST
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DATA BUS I/O CONTROL



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PAGE 8 OF 62

INSIGHT TELECAS

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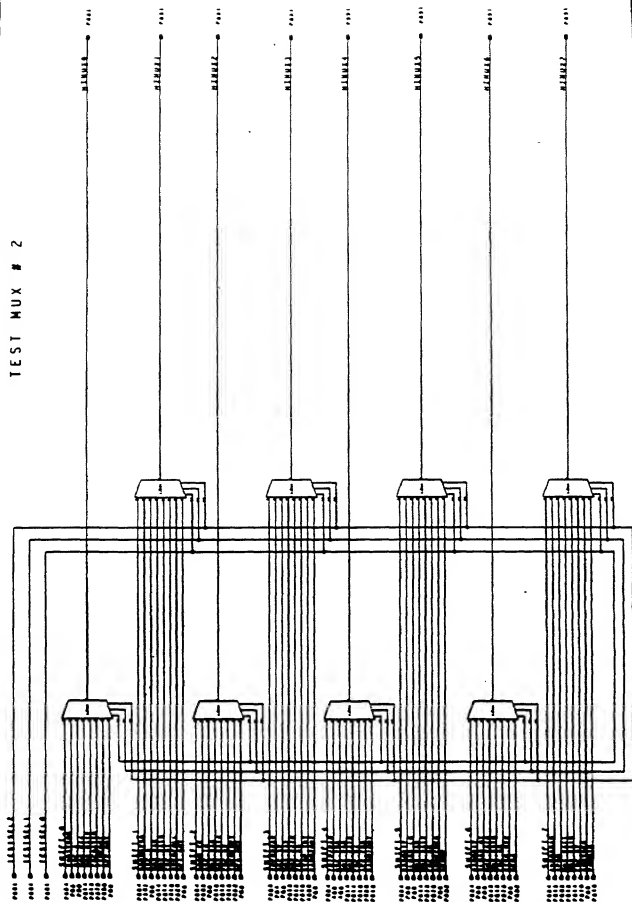
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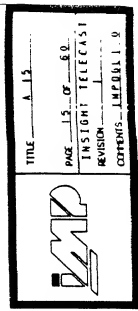
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PAGE 11 OF 69
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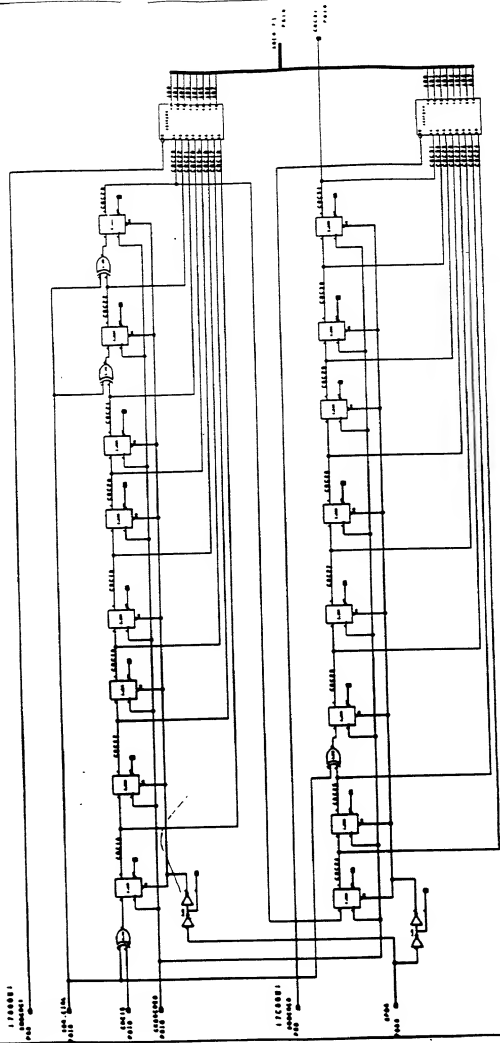
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SCHEMATIC DBE 1200
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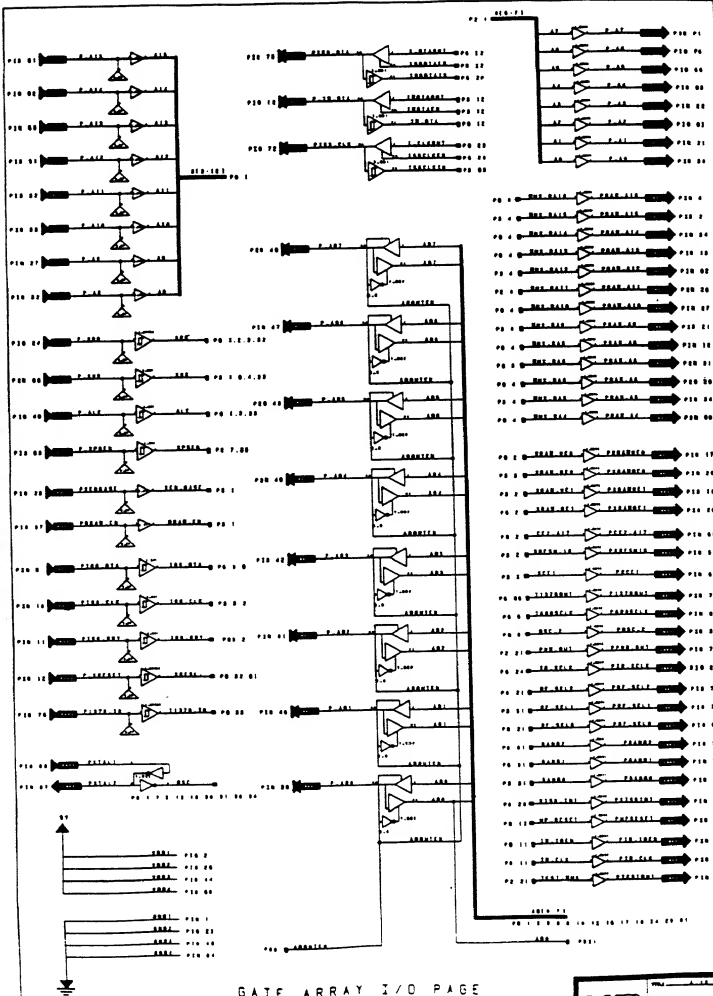


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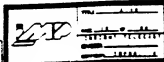
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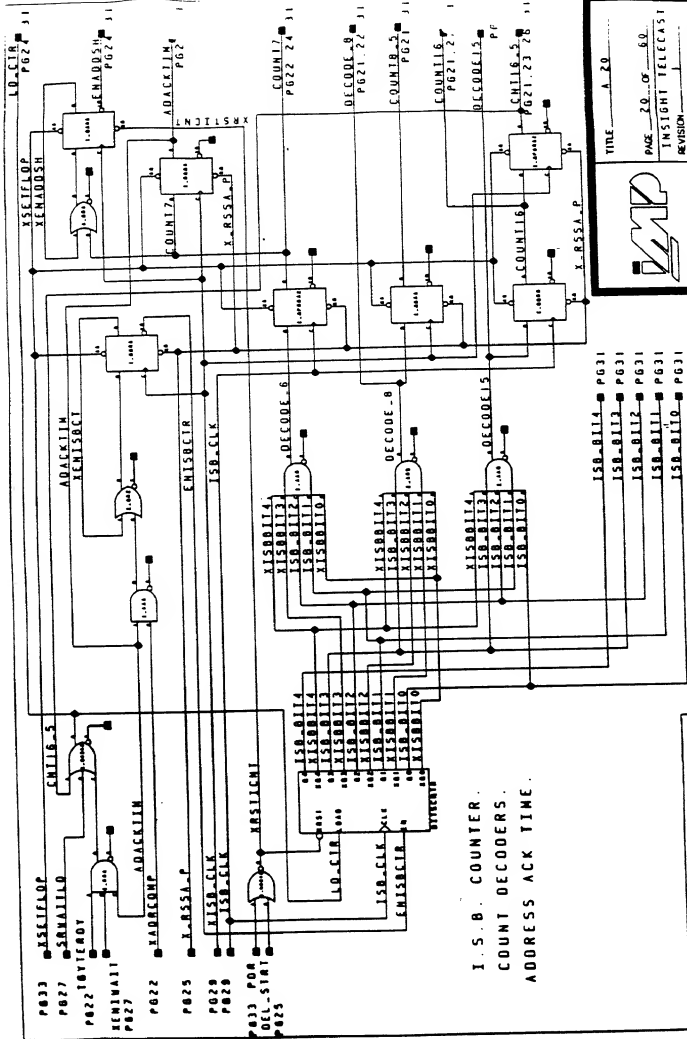
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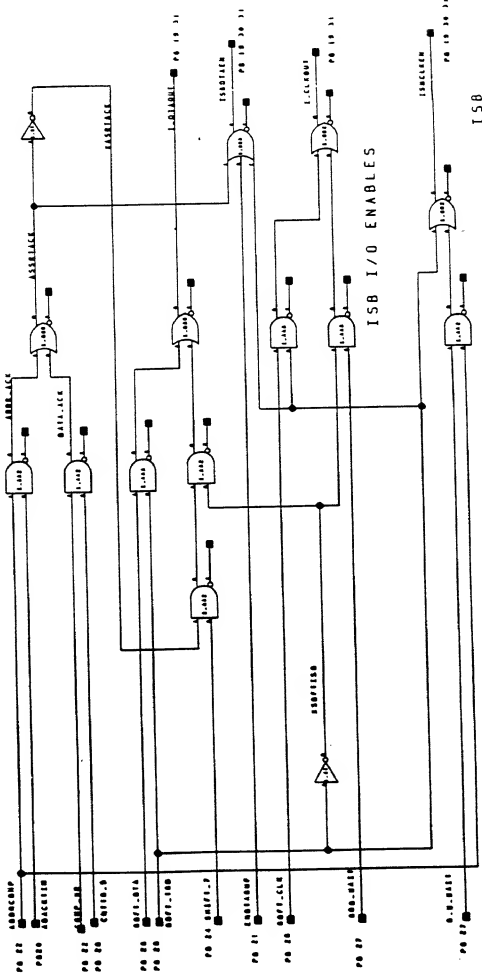
PAGE 30 OF 60

INSIGHT TELECAST

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СЛУЖБА ПРОСТА

ASSERT ACK



ISB

ASSERT ACK AND ISB I/O LOGIC

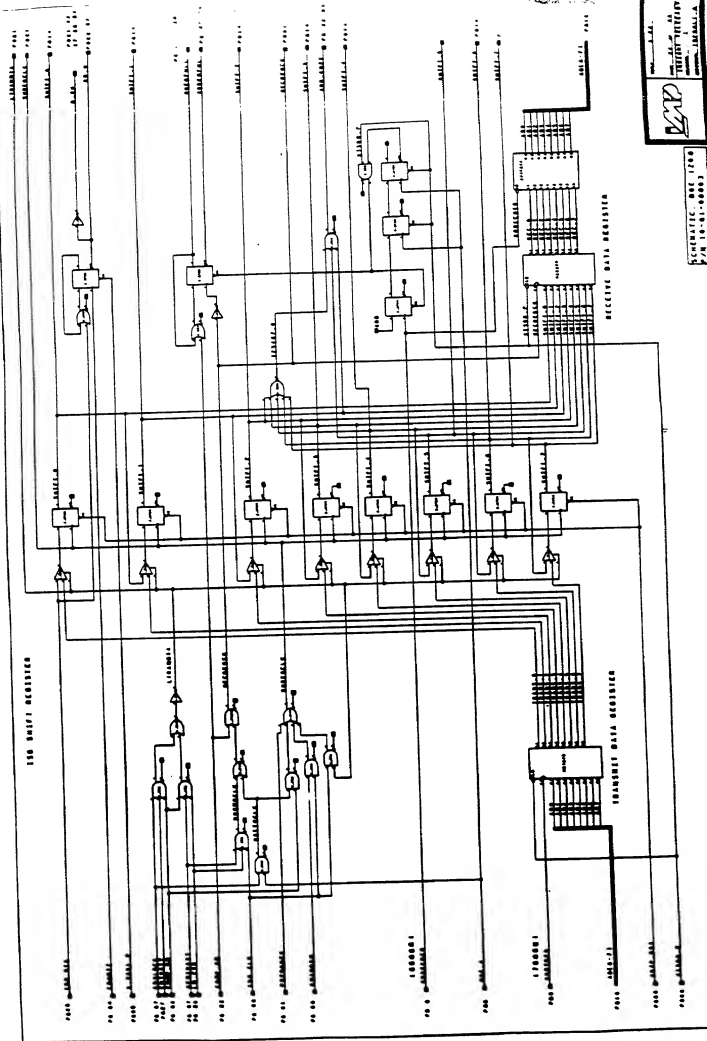
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SCHEMATIC DBE 1200
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TITLE A 23
PAGE 23 OF 60
INSTRUMENT TELECAST
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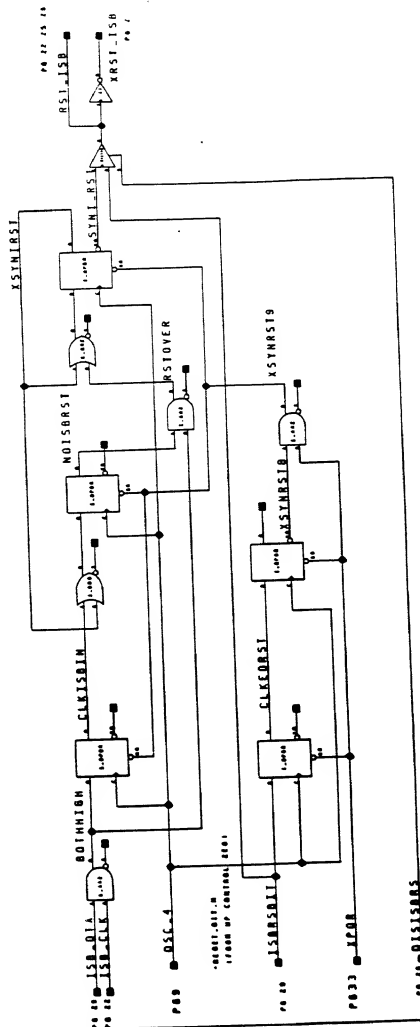
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RESET SYNCHRONIZER



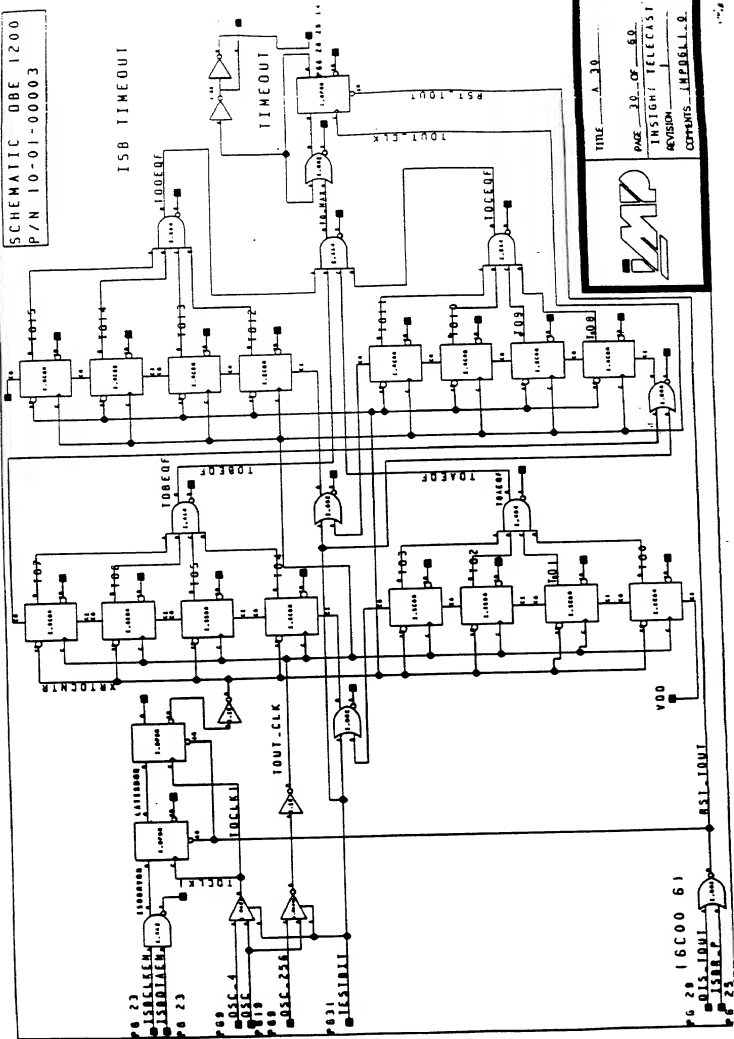
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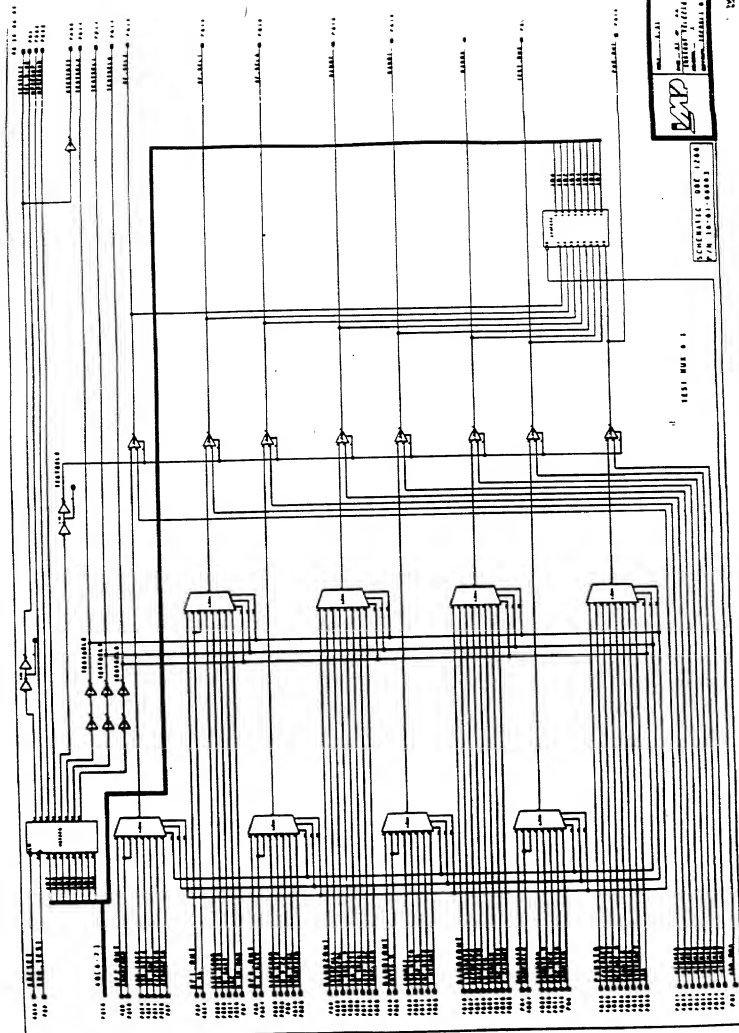
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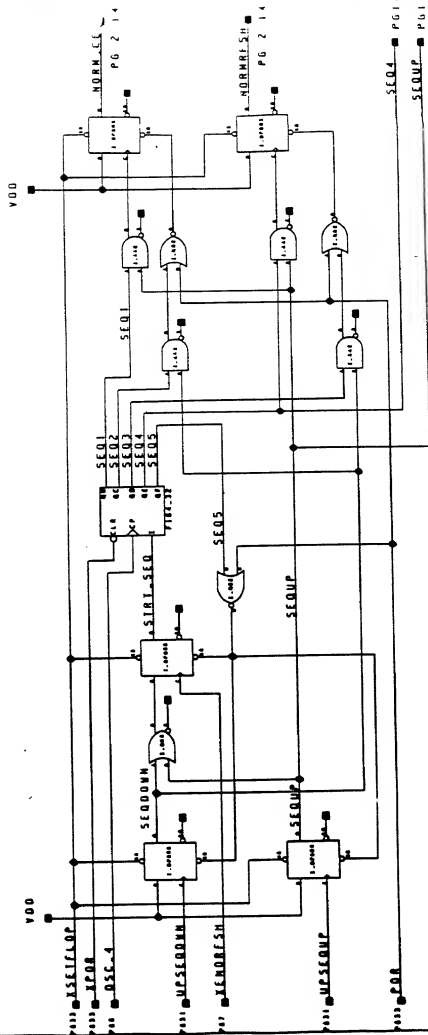
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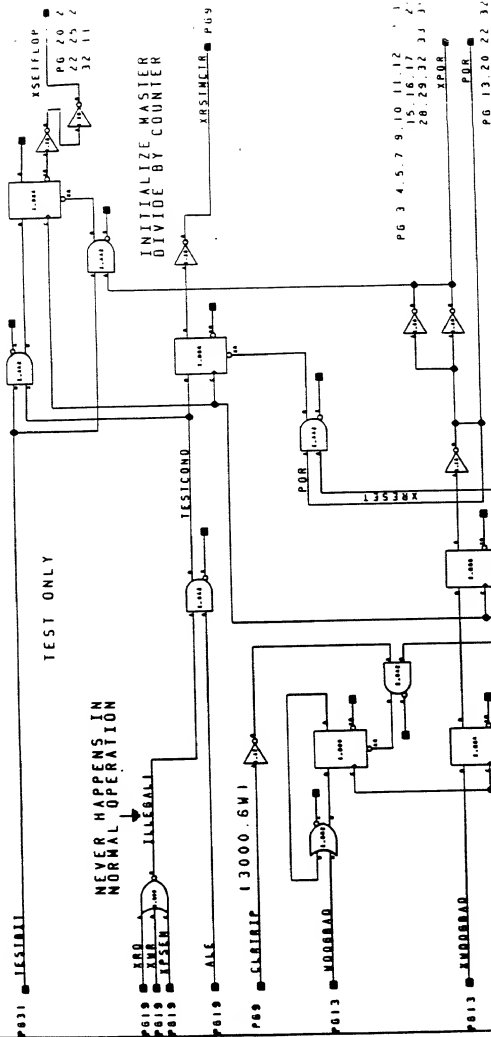


PSRAM SEQUENCE UP/DOWN LOGIC

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P/N 10-01-00003

PRESET FLIP FLOPS



SYNCHRONIZE INTERNAL POR TO SYSTEM CLOCK



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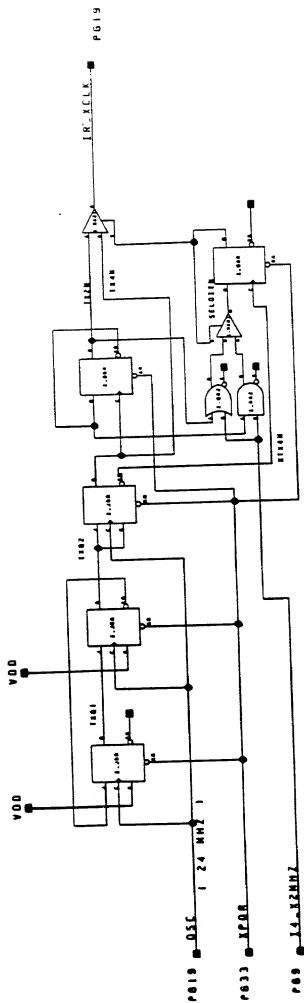
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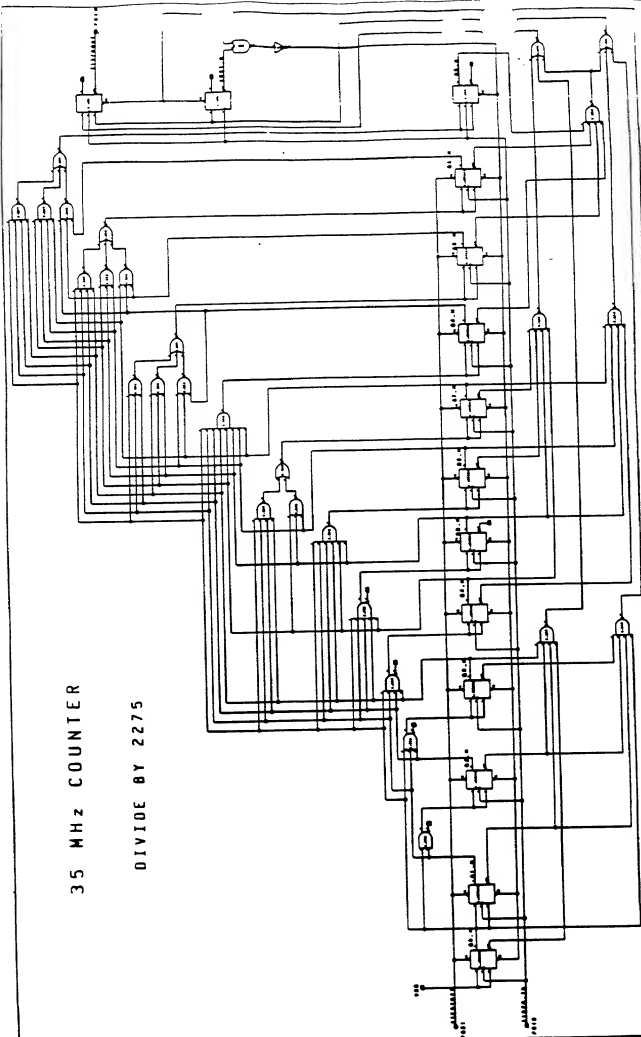
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SCHEMATIC DBE 1200
P/N 10-01-00003



35 MHz COUNTER

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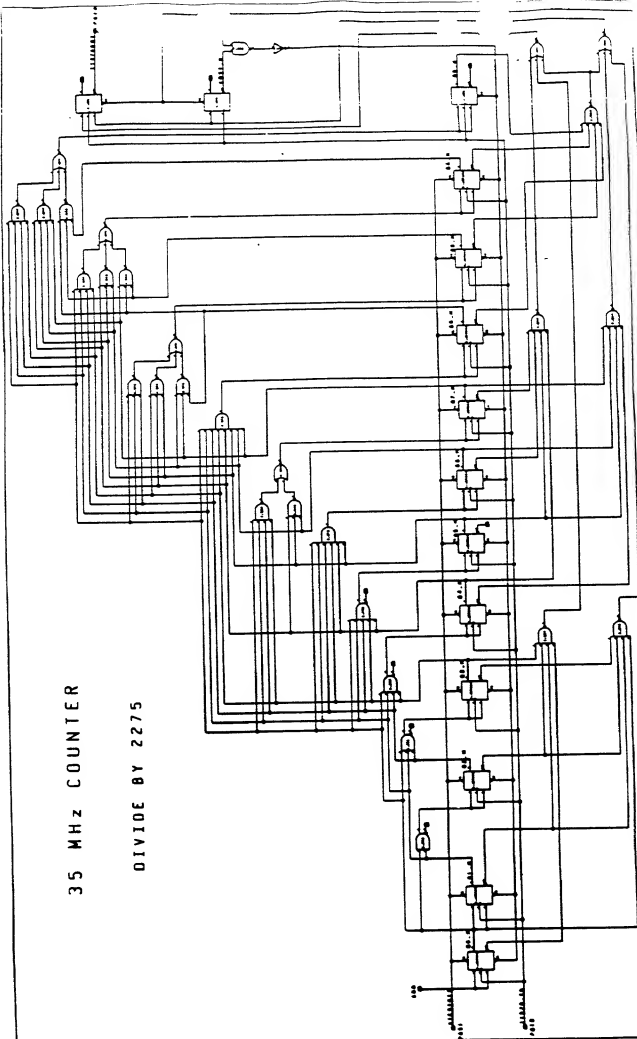


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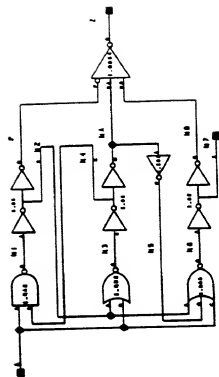
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Page 1 of 1
 35 MHz COUNTER
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35 MHz COUNTER
 DIVIDE BY 2275
 P/N 10-01-00003

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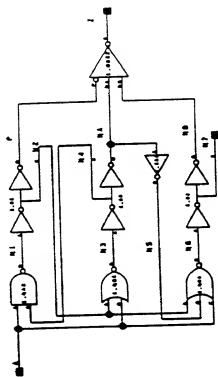
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P/N 10-01-0003



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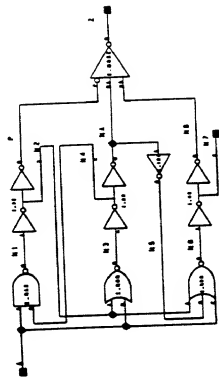
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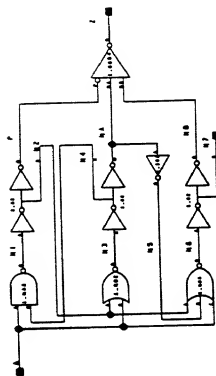
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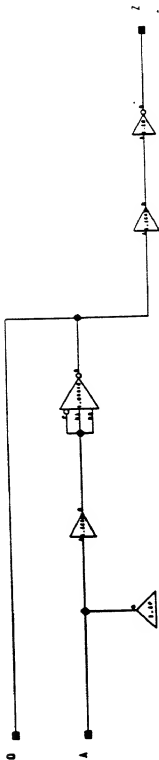
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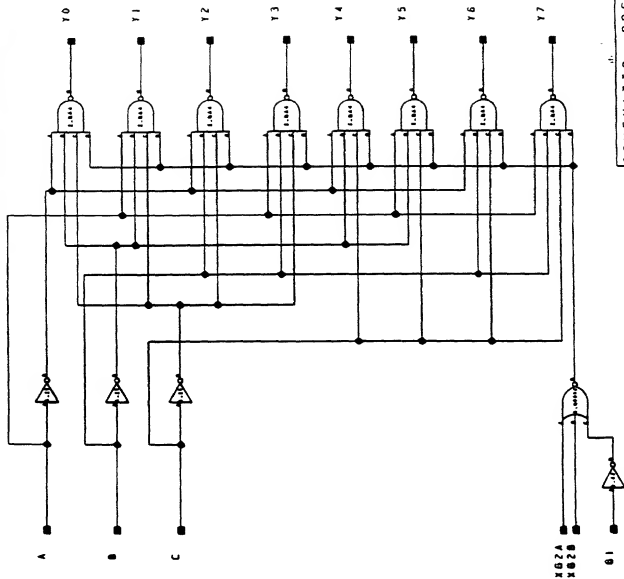


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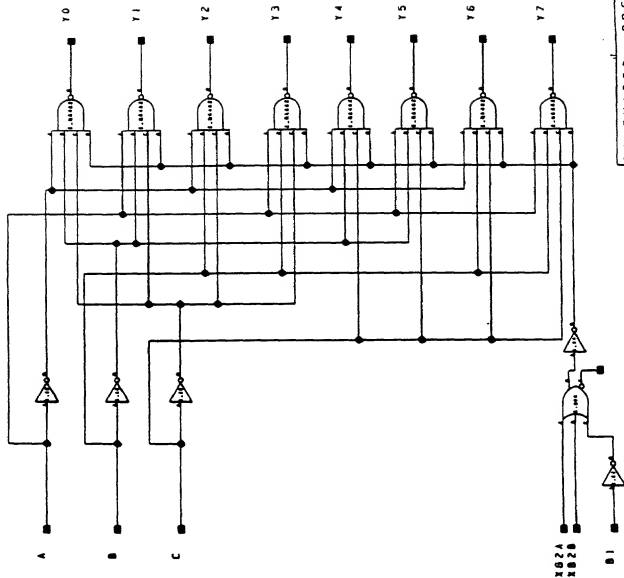
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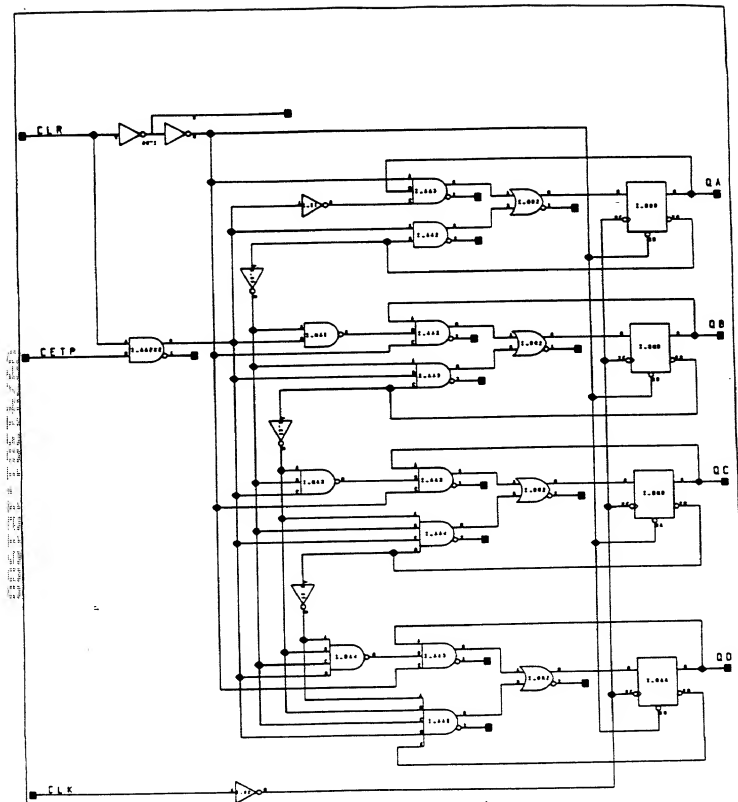
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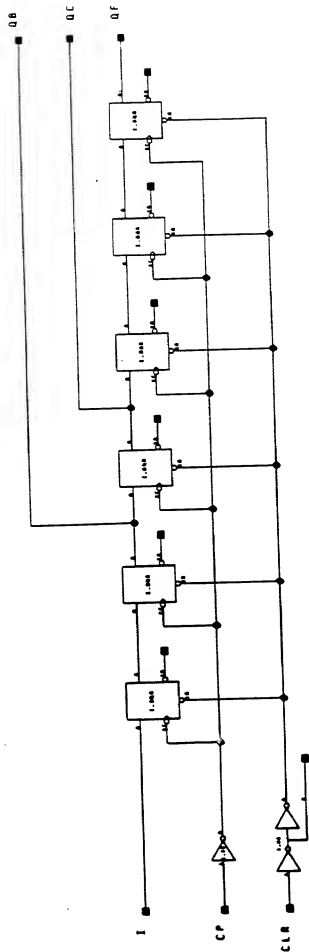


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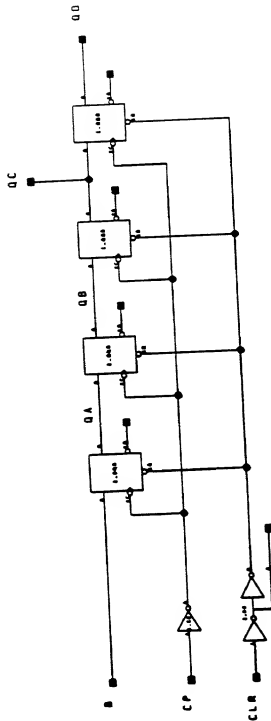
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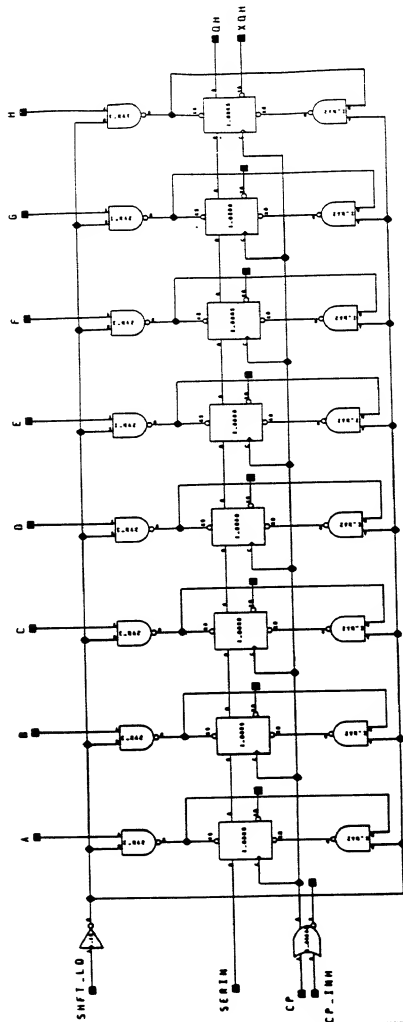


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SCHEMATIC DBE 1200
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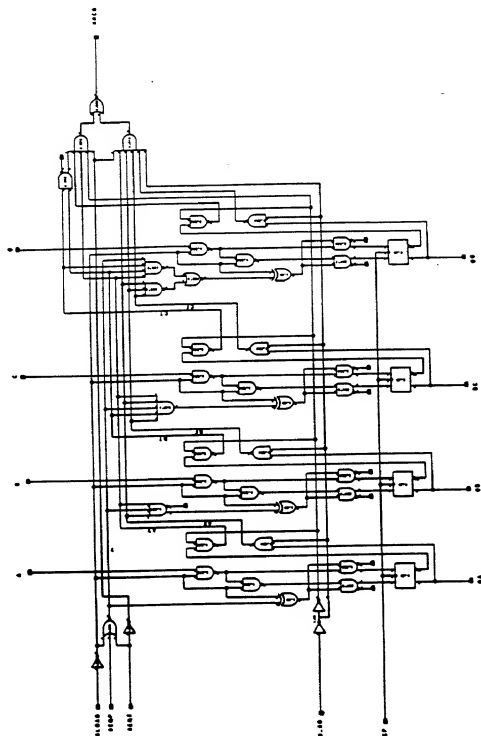
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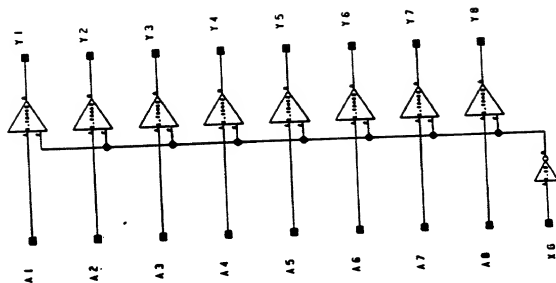


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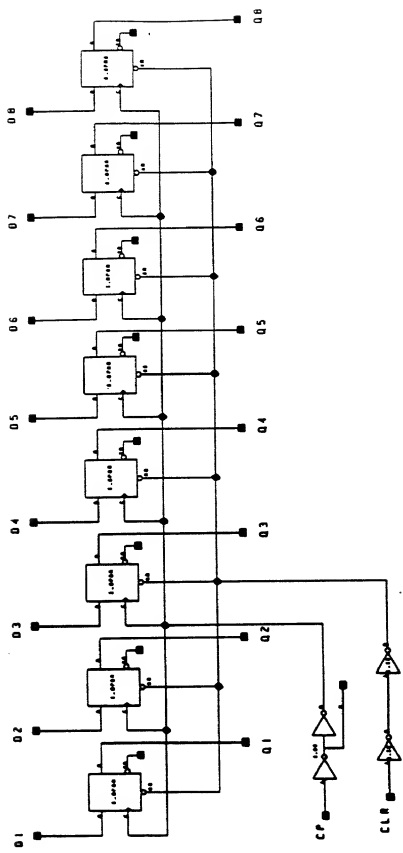


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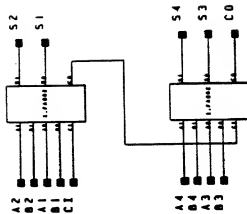
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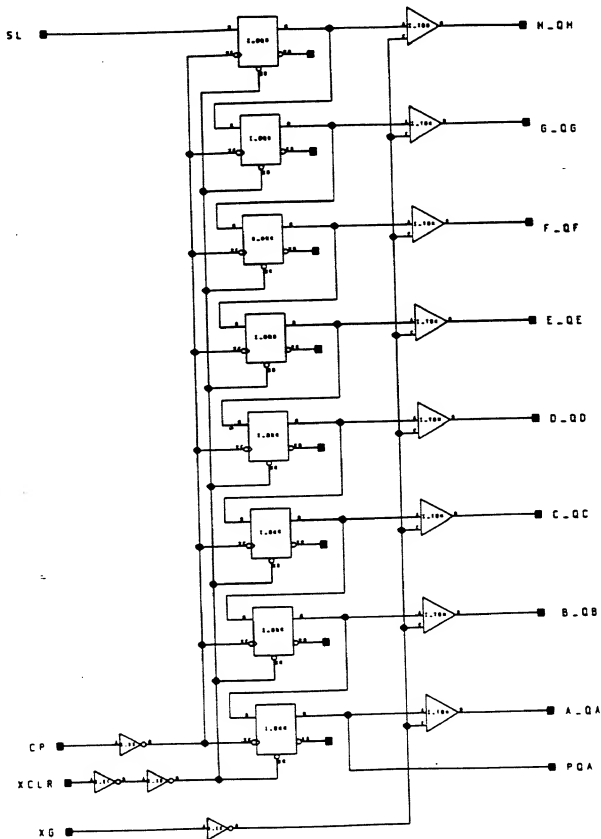
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| PAGE | 52 OF 60 |
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SCHEMATIC, OBE 1200
 P/N 10-01-00003

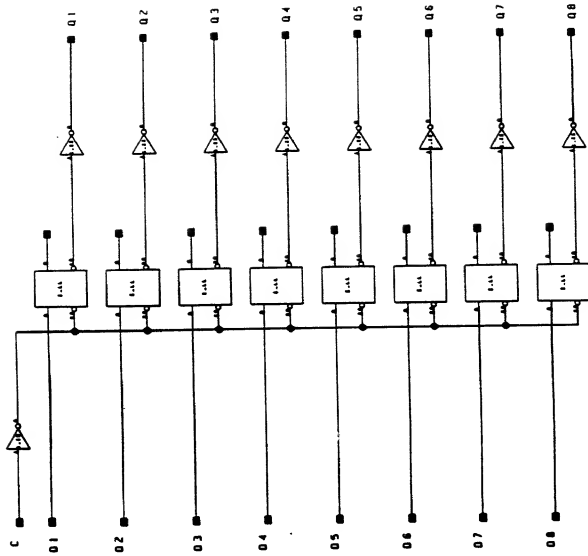


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PAGE 34 OF 60
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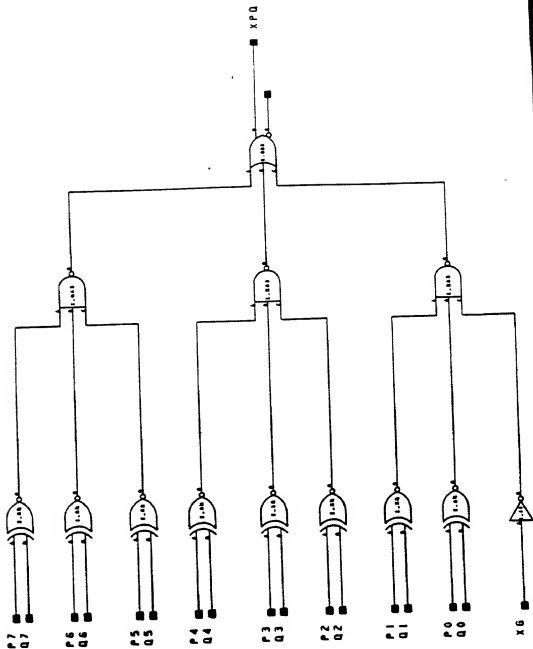
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 PAGE 55 OF 80
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SCHEMATIC, DBE 1200
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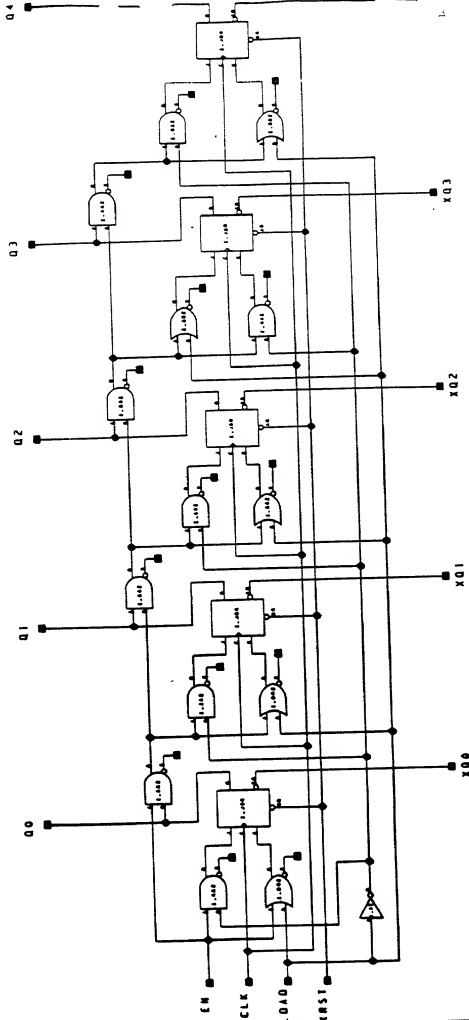
1. This schematic is for the purpose of illustrating the logic of the system and is not to be used for the purpose of manufacturing or for the purpose of testing the system.



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INSIGHT TELECAST
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SCHEMATIC DBE 1200
P/N 10-01-00003

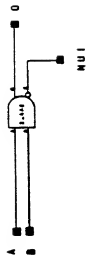
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 PAGE 57 OF 60
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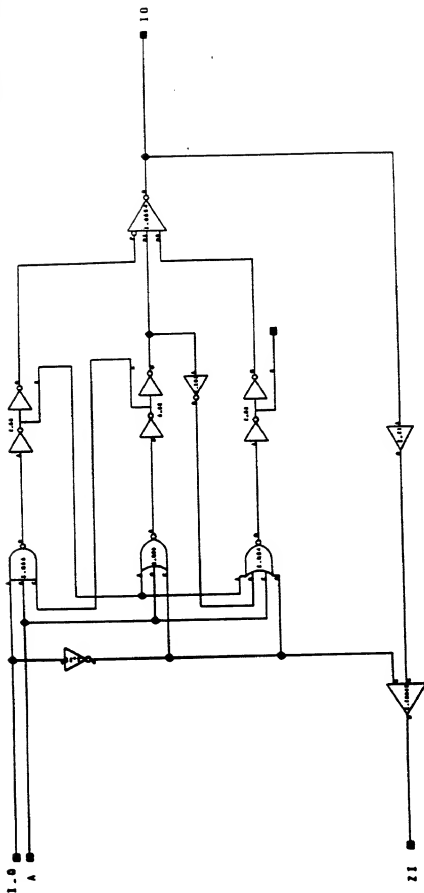


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SCHEMATIC DBE 1200
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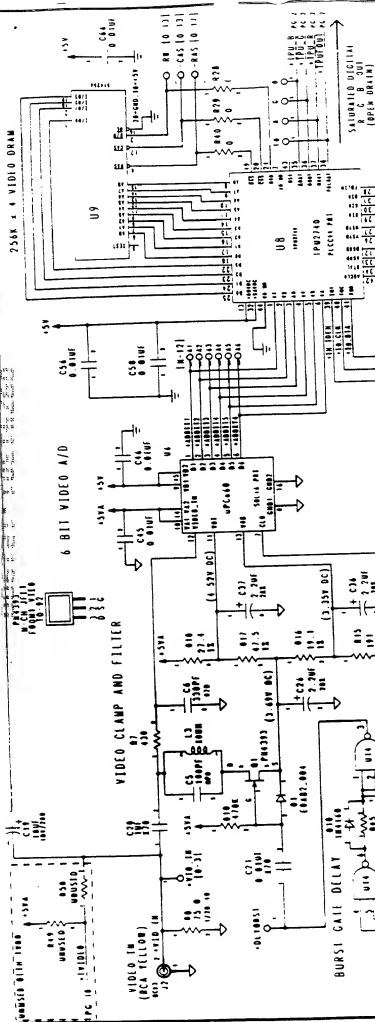
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CONTENTS 1-802

SCHEMATIC DBE 1200
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2546 x 4 VIDEO DRAM



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NOTE: SEE PCB COMPONENT LOCATIONS

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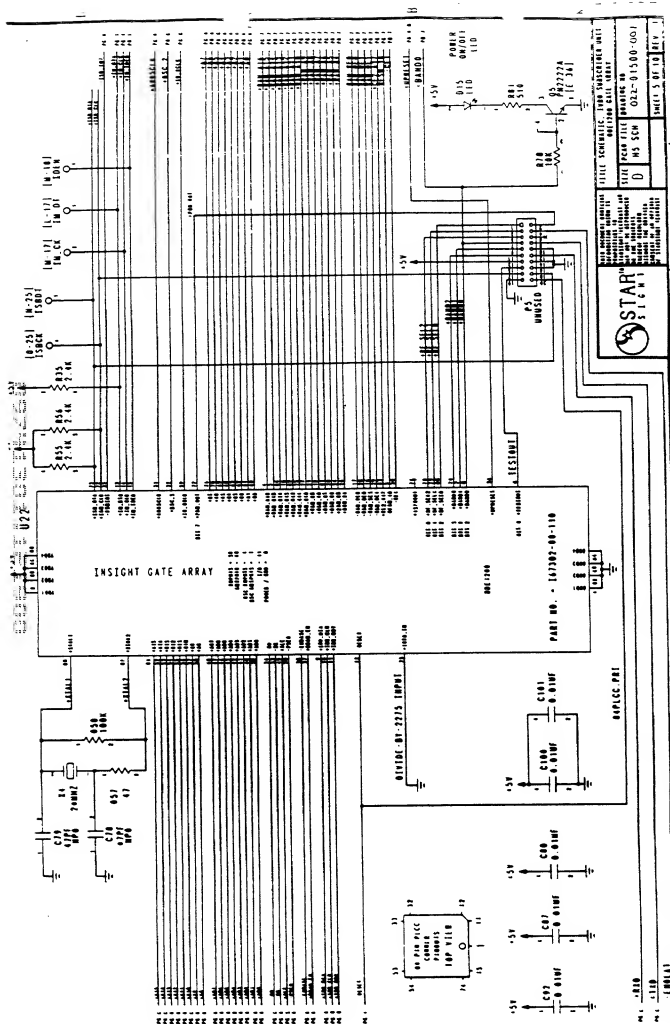
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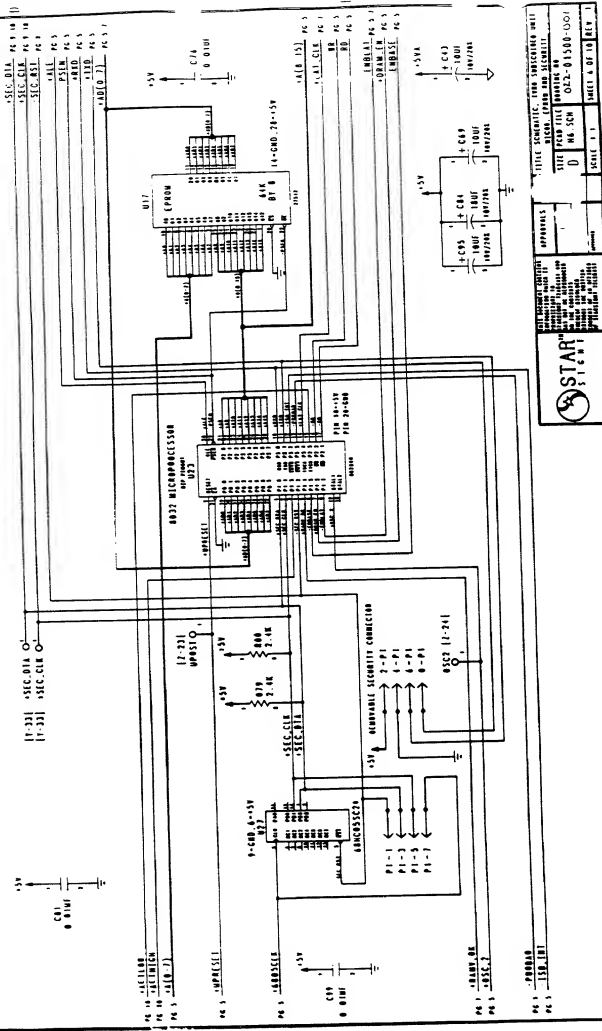
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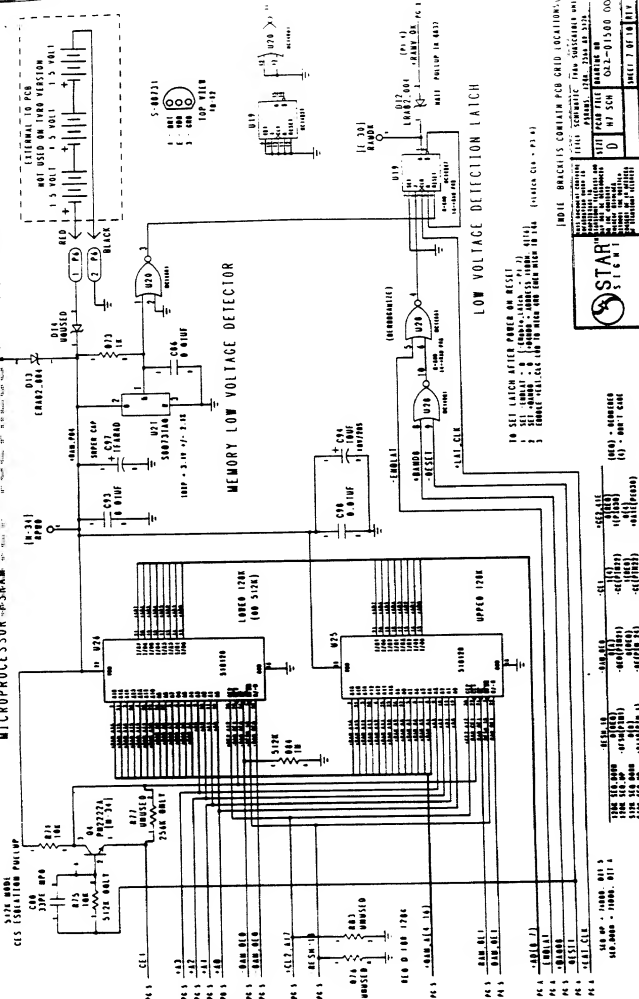
1. This drawing is a schematic diagram of the Star-1000 computer system. It shows the internal components and their interconnections. The components include the 8032 Microprocessor, 8032 Memory, 8032 I/O, 8032 Control, 8032 Power, 8032 Timing, 8032 Security, 8032 Status, 8032 Test, 8032 Maintenance, 8032 Service, 8032 Repair, 8032 Replacement, 8032 Upgrade, 8032 Expansion, 8032 Configuration, 8032 Initialization, 8032 Shutdown, 8032 Restart, 8032 Reset, 8032 Abort, 8032 Error, 8032 Warning, 8032 Alarm, 8032 Notification, 8032 Action, 8032 Response, 8032 Recovery, 8032 Completion, 8032 Success, 8032 Failure, 8032 Exception, 8032 Interrupt, 8032 Event, 8032 Signal, 8032 Message, 8032 Data, 8032 Information, 8032 Knowledge, 8032 Understanding, 8032 Awareness, 8032 Perception, 8032 Sensation, 8032 Feeling, 8032 Emotion, 8032 Thought, 8032 Idea, 8032 Concept, 8032 Theory, 8032 Hypothesis, 8032 Model, 8032 Framework, 8032 Structure, 8032 System, 8032 Mechanism, 8032 Process, 8032 Procedure, 8032 Method, 8032 Technique, 8032 Approach, 8032 Strategy, 8032 Plan, 8032 Policy, 8032 Rule, 8032 Regulation, 8032 Standard, 8032 Norm, 8032 Custom, 8032 Tradition, 8032 Practice, 8032 Habit, 8032 Routine, 8032 Pattern, 8032 Style, 8032 Fashion, 8032 Trend, 8032 Movement, 8032潮流, 8032 Wave, 8032 Current, 8032 Stream, 8032 Flow, 8032 Path, 8032 Route, 8032 Direction, 8032 Course, 8032 Journey, 8032 Voyage, 8032 Expedition, 8032 Adventure, 8032 Quest, 8032 Search, 8032 Hunt, 8032 Pursuit, 8032 Struggle, 8032 Fight, 8032 Battle, 8032 War, 8032 Conflict, 8032 Dispute, 8032 Controversy, 8032 Debate, 8032 Discussion, 8032 Conversation, 8032 Dialogue, 8032 Exchange, 8032 Interaction, 8032 Relationship, 8032 Connection, 8032 Link, 8032 Tie, 8032 Bond, 8032 Connection, 8032 Relationship, 8032 Interaction, 8032 Exchange, 8032 Dialogue, 8032 Conversation, 8032 Discussion, 8032 Debate, 8032 Controversy, 8032 Dispute, 8032 Conflict, 8032 War, 8032 Battle, 8032 Fight, 8032 Struggle, 8032 Pursuit, 8032 Hunt, 8032 Search, 8032 Quest, 8032 Adventure, 8032 Expedition, 8032 Voyage, 8032 Journey, 8032 Course, 8032 Direction, 8032 Path, 8032 Flow, 8032 Stream, 8032 Current, 8032 Wave, 8032潮流, 8032 Movement, 8032 Trend, 8032 Fashion, 8032 Style, 8032 Pattern, 8032 Routine, 8032 Habit, 8032 Practice, 8032 Tradition, 8032 Custom, 8032 Norm, 8032 Standard, 8032 Regulation, 8032 Rule, 8032 Policy, 8032 Strategy, 8032 Approach, 8032 Technique, 8032 Method, 8032 Procedure, 8032 Process, 8032 Mechanism, 8032 System, 8032 Framework, 8032 Model, 8032 Hypothesis, 8032 Theory, 8032 Concept, 8032 Idea, 8032 Thought, 8032 Emotion, 8032 Feeling, 8032 Sensation, 8032 Perception, 8032 Awareness, 8032 Understanding, 8032 Knowledge, 8032 Information, 8032 Data, 8032 Message, 8032 Signal, 8032 Event, 8032 Interrupt, 8032 Exception, 8032 Failure, 8032 Success, 8032 Completion, 8032 Recovery, 8032 Action, 8032 Notification, 8032 Alarm, 8032 Warning, 8032 Error, 8032 Abort, 8032 Reset, 8032 Restart, 8032 Shutdown, 8032 Initialization, 8032 Configuration, 8032 Expansion, 8032 Upgrade, 8032 Replacement, 8032 Repair, 8032 Service, 8032 Maintenance, 8032 Test, 8032 Status, 8032 Security, 8032 Timing, 8032 Power, 8032 I/O, 8032 Memory, 8032 Microprocessor.



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SHEET 1 OF 18 |

MICROPROCESSOR SYSTEM

USE APPROPRIATE TEST TO DETERMINE



STAR SYSTEM

10 SET LATCH AFTER POWER ON RESET

1 SET LATCH - 0 (CLEAR) - 1 (SET) - 1 (SET) - 1 (SET)

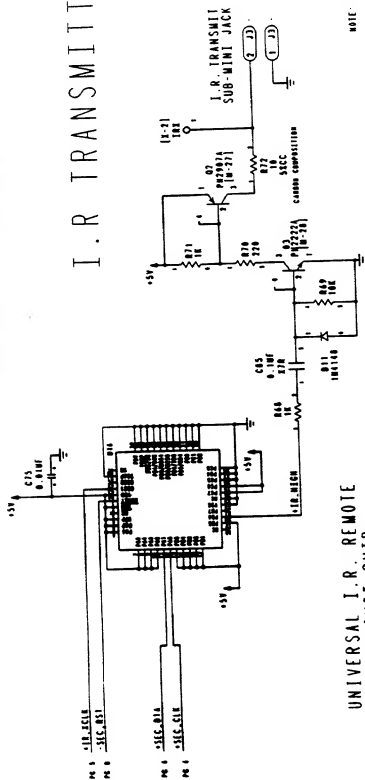
2 SET LATCH - 0 (CLEAR) - 1 (SET) - 1 (SET) - 1 (SET)

3 SET LATCH - 0 (CLEAR) - 1 (SET) - 1 (SET) - 1 (SET)

TABLE: BRACKET IS CONTAIN PCB GRID LOCATION

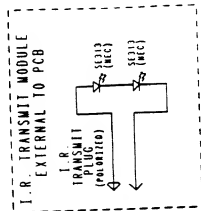
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| 59 | 10 | 100 512K |
| 60 | 10 | 100 512K |
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| 94 | 10 | 100 512K |
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| 96 | 10 | 100 512K |
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| 98 | 10 | 100 512K |
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| 100 | 10 | 100 512K |

I. R. TRANSMITTER



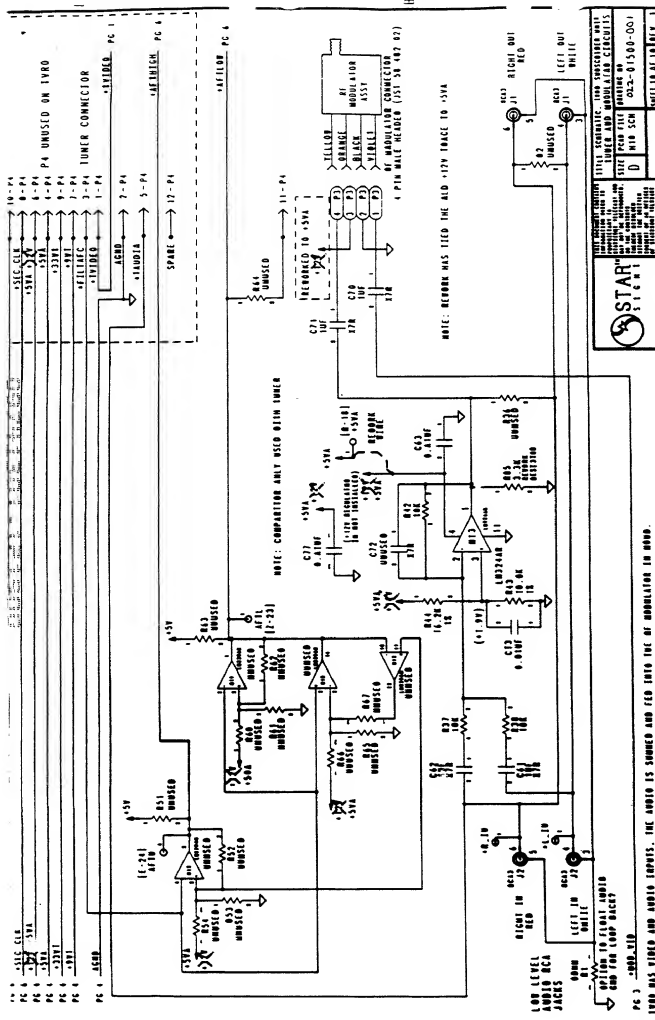
UNIVERSAL I.R. REMOTE
TRANSMIT CHIP

I. R. DRIVER



DATE: 11/09/2011 12:00 - 12:00 AM

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| STARSM
S I G N E T | |
| MOST IMPORTANT FACTORS:
Information useful to:
1. <input type="checkbox"/> Planning
2. <input type="checkbox"/> Research
3. <input type="checkbox"/> Design
4. <input type="checkbox"/> Development
5. <input type="checkbox"/> Production
6. <input type="checkbox"/> Marketing
7. <input type="checkbox"/> Other: _____ | |
| TITLE: _____ | |
| SIZE: _____ | |
| DATE: _____ | |
| BY: _____ | |
| FOR: _____ | |
| COMMENTS: _____ | |



STAR
ELECTRONICS

100M VIDEO AND AUDIO INPUTS. THE AUDIO IS SUMMED AND FED INTO THE RF MODULATOR IN MOD.

100M VIDEO AND AUDIO INPUTS. THE AUDIO IS SUMMED AND FED INTO THE RF MODULATOR IN MOD.

100M VIDEO AND AUDIO INPUTS. THE AUDIO IS SUMMED AND FED INTO THE RF MODULATOR IN MOD.